Special Issue

High-Performance Computer Architecture

Message from the Guest Editors

Computing architectures are experiencing a surge in interest, driven by the need to achieve high performance at a reduced energy cost, as highlighted by the "power wall" experienced in supercomputer design and by the need to maximize performance at the edge. Al and other emerging technologies have led to a range of tailored architectural solutions, exploiting specific characteristics of an application class to increase performance and reduce energy consumption. At the edge, the need for high performance meets other more stringent requirements such as real-time constraints. To address these issues, a range of tools are needed at the architectural, electronic design automation, and system software levels to enable the customization, co-design, and co-optimization of applications and architectures. This Special Issue aims to collect manuscripts presenting solutions and tools for improving performance in a range of different application classes, in the context of both supercomputers and highperformance embedded systems.

Guest Editors

- Dr. Giovanni Agosta
- Dr. Daniele Cattaneo
- Dr. Stefano Cherubin

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

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