Special Issue

Blockchain and Artificial Intelligence for Next-Generation Network and Cybersecurity

Message from the Guest Editors

This Special Issue seeks to explore how the integration of blockchain and Al can provide robust, intelligent, and scalable solutions for the security and reliability of future communication networks. The focus is on advancing the state of knowledge at the intersection of these technologies to address pressing challenges in both networking and cybersecurity domains.

The scope covers a wide range of research directions. Relevant topics include blockchain protocols for trustworthy and auditable data exchange in 5G and 6G environments, Al-enhanced intrusion detection and anomaly detection in vehicular and edge networks, privacy-preserving federated learning for distributed network infrastructures, decentralized identity and trust management for loT ecosystems, and adversarial robustness for intelligent network services.

By bringing together advances in next-generation networks, cybersecurity, and privacy, this Special Issue will identify synergies, open challenges, and design patterns that can guide the development of trustworthy digital infrastructures at scale.

Guest Editors

Dr. Guangsheng Yu

School of Electrical and Data Engineering, University of Technology Sydney, 15 Broadway, Ultimo, Sydney, NSW 2007, Australia

Dr. Xu Wang

School of Electrical and Data Engineering, University of Technology Sydney, 15 Broadway, Ultimo, Sydney, NSW 2007, Australia

Dr. Qin Wang

CSIRO Data61, Sydney, Australia

Deadline for manuscript submissions

15 April 2026



Electronics

an Open Access Journal by MDPI

Impact Factor 2.6 CiteScore 6.1



mdpi.com/si/254483

Electronics
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
electronics@mdpi.com

mdpi.com/journal/electronics





Electronics

an Open Access Journal by MDPI

Impact Factor 2.6 CiteScore 6.1



About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and Telecommunications, Politecnico di Torino, 10129 Torino, Italy

Author Benefits

High Visibility:

indexed within Scopus, SCIE (Web of Science), CAPlus / SciFinder, Inspec, Ei Compendex and other databases.

Journal Rank:

JCR - Q2 (Engineering, Electrical and Electronic) / CiteScore - Q1 (Electrical and Electronic Engineering)

Rapid Publication:

manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).

