Special Issue

Digital and Analog Circuits and Applications Based on TFET Transistors

Message from the Guest Editors

The design of circuits aiming at exploiting the steep turn-on transition of TFETs is currently of great interest. The aim of this Special Issue is to collect reports on TFET potentiality to outperform CMOS at low voltages, with an emphasis on TFET circuit topologies for memory cells, digital logic, and analog and mixed-signal architectures. Papers discussing circuit design criteria and strategies to overcome TFET issues are also welcome. Topics of Interest:

- Tunneling Field Effect Transistors (Tunnel FETs, T-FETs);
- T-FET circuits for energy efficient computing and information processing;
- Energy-Efficient computing and information processing with T-FET transistor circuits and architectures;
- TFET-based memories;
- Mixed T-FET CMOS designs.

Welcome to contribute.

Guest Editors

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Deadline for manuscript submissions

closed (31 December 2021)

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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

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manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).