

Special Issue

Advances in System-on-Chip Design

Message from the Guest Editor

The main aim of this Special Issue is to seek high-quality submissions that address emerging challenges in complex SoC architectures, such as the design of specialized accelerators, their integration with pre-existing components, the design of scalable and efficient memory architectures, and the identification of additional vulnerabilities arising from the integration of several different components. The topics of interest include but are not limited to:

- Novel SoC architectures and design methodologies;
- Layout- and energy-driven SoC architectures;
- Design space exploration methods for SoC design;
- High power density dower electronic systems;
- High-level synthesis and hardware/software co-design methodologies;
- Efficient memory architectures for scalable and distributed SoC architectures.;
- Hardware security and hardware-assisted security in SoC architectures.

Guest Editor

Dr. Christian Pilato

Dipartimento di Elettronica, Informazione e Bioingegneria, Politecnico di Milano, 20133 Milano, Italy

Deadline for manuscript submissions

closed (31 December 2021)



Electronics

an Open Access Journal
by MDPI

Impact Factor 2.6
CiteScore 6.1



mdpi.com/si/58453

Electronics
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
electronics@mdpi.com

[mdpi.com/journal/
electronics](https://mdpi.com/journal/electronics)





Electronics

an Open Access Journal
by MDPI

Impact Factor 2.6
CiteScore 6.1



[mdpi.com/journal/
electronics](https://mdpi.com/journal/electronics)



About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and Telecommunications, Politecnico di
Torino, 10129 Torino, Italy

Author Benefits

High Visibility:

indexed within Scopus, SCIE (Web of Science), CAPlus /
SciFinder, Inspec, Ei Compendex and other databases.

Journal Rank:

JCR - Q2 (Engineering, Electrical and Electronic) /
CiteScore - Q1 (Electrical and Electronic Engineering)

Rapid Publication:

manuscripts are peer-reviewed and a first decision is
provided to authors approximately 16.8 days after
submission; acceptance to publication is undertaken in 2.4
days (median values for papers published in this journal in
the first half of 2025).