

Special Issue

Recent Advances in Silicon-Based RFIC Design

Message from the Guest Editor

Aim of this Special Issue is to collect recent advances in the design of silicon-based RFIC's and related systems covering, but not limited to, the following hot application fields:

- Digital intensive RF transceiver architectures
- High speed ADCs for direct sampling RF receiver architectures
- Millimeter-wave SiGe BiCMOS front-ends and frequency synthesis
- Cryogenic CMOS RFICs for quantum engineering
- Silicon microwave-photonics integrated circuits

Welcome to contribute!

Guest Editor

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Deadline for manuscript submissions

closed (1 November 2022)



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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

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Rapid Publication:

manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).