

Special Issue

Software/Hardware Codesign for Embedded Multicore Systems

Message from the Guest Editor

This Special Issue calls for innovative work to design, analyze, optimize the use, and solve the challenges of using multicore systems using software/hardware codesign approaches. Topics of interest include, but are not restricted to the following:

- HW/SW partitioning, interfaces and synthesis
- Handling heterogeneity in codesigned multicores
- Memory hierarchies, scratchpad, and caches in multicore systems
- Communication and synchronization for multicore systems
- Modeling, analysis, and multi-criteria optimizations of non-functional properties
- Security, dependability, and fault tolerance of SW/HW-codesigned multicores
- Automatic parallelization and compilation approaches for multicores
- SW/HW codesign for in- and near-memory computing
- Hypervisor and operating systems for multicores
- Architecture-compiler-operating system codesign
- Runtime adaptive and reconfigurable systems
- WCET/WCEC analysis for multicore systems
- Networks-on-Chip (NoCs) for codesigned multicore systems
- Design space exploration, virtual platforms, and cosimulation

Guest Editor

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Deadline for manuscript submissions

closed (31 December 2020)



Electronics

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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

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