

Special Issue

Design Tools and Architectures for Coarse-Grained Reconfigurable Computing

Message from the Guest Editors

The objective of this Special Issue is to bring together the latest advances in coarse-grained reconfigurable computing (CGRC), including design tools, architectures, and applications. CGRC topics include, but are not limited to:

- Coarse-grained reconfigurable architectures;
- Fine-grained versus coarse-grained computing;
- Spatial and temporal computation in CGRA;
- Run-time reconfiguration/adaptation of CGRA;
- Models of computation for CGRA;
- Programming tools and compilers for CGRA;
- Energy-aware compilation for CGRA;
- Simulation tools for CGRA;
- CGRA for domain-specific applications (image processing, deep-learning, network processing, etc.);
- Edge computing and high-performance computing with CGRA.

Please click [here](#) to find information!

Welcome to contribute!

Guest Editors

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Deadline for manuscript submissions

closed (1 May 2022)



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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

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