

Special Issue

RF and Mixed Signal High Speed Circuit Design

Message from the Guest Editors

This Special Issue aims to publish recent advances on RF and mixed signal circuits, giving more attention and focus on front-end transmitter and receiver high frequency circuits, circuits for front-end optical processing, A/D and D/A high-speed low consumption devices and high-speed data links, interfaces, and backplanes. Representative topics of interest include the following:

- Front-end transmitter and receiver circuits (PAs, LNAs, mixers, oscillators)
- Front-end optical processing circuits (transimpedance amplifiers, limiters, modulators)
- Wideband A/D and D/A converters, sample and hold / track and hold amplifiers
- Techniques for system-on-chip (SoC) development of multiple radios, high performance FPGA interfaces
- High speed interfaces such as USB and PCIe
- Data serializers/ deserializers (SerDes), clock and data recovery circuits (CDRs)
- High speed links, differential line modeling, signal integrity
- Gigabit Ethernet transmission systems for vehicle networking, high throughput interconnects

Welcome to contribute!

Guest Editors

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Deadline for manuscript submissions

closed (31 July 2021)



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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

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