

Special Issue

Low Power RFIC Architectures for Emerging Wireless Standards

Message from the Guest Editors

The successful adoption of 5G and IoT standards, the coming standardization of 6G mobile communications, the continuous smartification of cities and integration of blockchain technology, the emerging vehicles connectivity and autonomous driving, all these push increased pressure on the practical development of more efficient RF architectures, able to cover multiple bands, at higher frequencies, and with improved power efficiency. To address such challenges, this Special Issue covers RFIC architectures with low power capability demonstrated on circuit or system level by means of prototype measurement and/or simulations. Wireless sensors and RFID implementations are not excluded. Extended versions of conference articles are accepted as long as they have at least 50% novelty.

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Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

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