Special Issue

Programming for Heterogeneous and Embedded Computing

Message from the Guest Editors

Many computing platforms are currently combining different kinds of computing devices in the same system. They can include multicore CPUs with complex memory hierarchies, massive accelerators such as GPUs, specialized computing units for specific fields such as deep learning, or reconfigurable hardware devices such as FPGAs. These different types of devices are combined to build a range of systems from high-end supercomputers to embedded consumer-electronics systems. The aim of this Special Issue is to promote advancement in the following topics:

- Heterogeneous programming, including multicore CPUs, accelerators, and/or domain specific architectures;
- Parallel programming models and languages;
- High-level system synthesis techniques;
- High-performance compiling and code generation;
- Run-time systems for heterogeneous platforms;
- Parallel programming, debugging, and profiling tools;
- Programming for resilience;
- Code and performance portability;
- Reconfigurable and embedded computing;
- New programming and computing paradigms.

Guest Editors

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

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