Special Issue

Recent Advances in CMOS Logic Circuits

Message from the Guest Editors

With the rapid development of process technology, CMOS digital logic circuits have also made new developments. Facing the current diversified world, artificial intelligence, the Internet of Things, biomedical chips, communication signal processing chips. electronic equipment for autonomous vehicles, and other related research topics will become the focus of current research. In ASIC design, many advanced designs are required in terms of area, speed, throughput, and power consumption. Therefore, this Special Issue will invite outstanding researchers on CMOS digital logic circuits from all over the world to submit the most advanced research topics. Although this Special Issue covers the topics of CMOS digital logic circuits, artificial intelligence, Internet of Things, biomedical chips, and advanced digital signal processing chips, some specific topics include but are not limited to:

- Artificial Intelligence Chip;
- loT chip;
- Biomedical chip;
- Advanced digital signal processing chip;
- Communication signal processing chip;
- Video/image signal processing chip

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Deadline for manuscript submissions

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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

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manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).

