

Special Issue

Low Power Circuits in Microelectronics

Message from the Guest Editors

This Special Issue focuses on innovative ideas in relation to the design and application of low-power microelectronics architectures. The current trend towards the integration on one single chip of a whole system requires low-power designs not only due to power budget and energy savings, but also because of autonomy in wearable or IoT devices, self-supplied circuits or the battery lifecycle. Additionally, deep circuit integration may presuppose a high energy dissipation per unit of area, incompatible with a proper performance. In those cases, low power consumption is mandatory. Finally, special attention must be paid to artificial intelligence and power handling. The topics of interest include but are not limited to:

- Wearable devices;
- Internet of Things;
- Automotive;
- Consumer electronics;
- Biomedical/Healthcare applications;
- Neuromorphic circuits;
- Deep-learning, neural networks, and machine learning hardware applications;
- FPGA-based digital design;
- Sensor applications;
- Power circuit and energy harvesting;
- Low-power design methodology/flow.

Guest Editors

Dr. Eric Gutierrez

Department of Electronics Technology, Carlos III University of Madrid,
28911 Leganés, Madrid, Spain

Prof. Dr. Susana Paton

Department of Electronics Technology, Carlos III University of Madrid,
28911 Leganés, Madrid, Spain

Deadline for manuscript submissions

closed (31 October 2021)



Electronics

an Open Access Journal
by MDPI

Impact Factor 2.6
CiteScore 6.1



mdpi.com/si/45747

Electronics
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
electronics@mdpi.com

[mdpi.com/journal/
electronics](https://mdpi.com/journal/electronics)





Electronics

an Open Access Journal
by MDPI

Impact Factor 2.6
CiteScore 6.1



[mdpi.com/journal/
electronics](https://mdpi.com/journal/electronics)



About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and Telecommunications, Politecnico di
Torino, 10129 Torino, Italy

Author Benefits

High Visibility:

indexed within Scopus, SCIE (Web of Science), CAPlus /
SciFinder, Inspec, Ei Compendex and other databases.

Journal Rank:

JCR - Q2 (Engineering, Electrical and Electronic) /
CiteScore - Q1 (Electrical and Electronic Engineering)

Rapid Publication:

manuscripts are peer-reviewed and a first decision is
provided to authors approximately 16.8 days after
submission; acceptance to publication is undertaken in 2.4
days (median values for papers published in this journal in
the first half of 2025).