Special Issue

Advanced Electronics and Optoelectronics Based on 2D Materials and vdW Heterostructures

Message from the Guest Editor

Two-dimensional (2D) materials have created a new paradigm in atomic-scale devices. Numerous applications such as logic devices and advanced optoelectronics have been extensively explored in relation to 2D materials owing to their compelling properties, including atomically thin thicknesses, dangling bond-free surfaces, and appropriate band gaps, etc. Moreover, the weak vdW interaction between the layers means that 2D materials can be easily exfoliated, and offers great flexibility to stack a wide variety of 2D materials to construct artificial vdW heterostructures without the constraint of atomically precise commensurability. These 2D vdW heterostructures have emerged as promising way of further engineering the electronic and optoelectronic properties of 2D materials, showing great promise for a variety of novel functional devices, including advanced transistors, multilevel inverters, memory devices, photodetectors, photovoltaics, etc. Motivated by the rapid progresses in 2D materials, this Special Issue highlights the material preparation, property modulation, device design, and their device explorations towards advanced electronics and optoelectronics.

Guest Editor

Dr. Wugang Liao

College of Electronics and Information Engineering, Shenzhen University, Shenzhen 518060, China

Deadline for manuscript submissions

closed (15 August 2023)



Electronics

an Open Access Journal by MDPI

Impact Factor 2.6 CiteScore 6.1



mdpi.com/si/153491

Electronics
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
electronics@mdpi.com

mdpi.com/journal/ electronics





Electronics

an Open Access Journal by MDPI

Impact Factor 2.6 CiteScore 6.1



About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and Telecommunications, Politecnico di Torino, 10129 Torino, Italy

Author Benefits

High Visibility:

indexed within Scopus, SCIE (Web of Science), CAPlus / SciFinder, Inspec, Ei Compendex and other databases.

Journal Rank:

JCR - Q2 (Engineering, Electrical and Electronic) / CiteScore - Q1 (Electrical and Electronic Engineering)

Rapid Publication:

manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).

