Special Issue

Design Principles, Methodologies, and Tools for Processing-in-Memory Architectures

Message from the Guest Editor

This Special Issue aims to address the development of models, principles, methodologies, design automation tools, and software stacks for PIM architectures (including computing-in-memory and near-memory architectures). Topics of interest include (but are not limited to) the following:

- PIM architectures and circuits for new applications;
- General-purpose PIM architectures and circuits;
- New computing models for PIM;
- Software–hardware and cross-layer co-design for PIM;
- Instruction set architecture (ISA) and compiler designs for PIM;
- Design automation tools for PIM, including architecture-level, circuit-level, and physical-level tools, etc;
- Simulation and verification tools for PIM;
- Reliability analysis and enhancement for PIM;
- Task mapping and scheduling for PIM.

Guest Editor

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Deadline for manuscript submissions

closed (15 April 2024)

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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

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