

Special Issue

Low-Power and High-Efficiency Circuit Design for Next-Generation Microchips

Message from the Guest Editors

This Special Issue aims to bring together recent advances in the design, modeling, and implementation of low-power and high-efficiency circuits and systems for next-generation microchips. We welcome original research and comprehensive review articles covering—but not limited to—energy-efficient data converters (SAR, pipelined-SAR, delta-sigma, time-domain ADCs/DACs), low-power analog and mixed-signal building blocks, high-speed serial link transceivers (PCIe, DDR, Ethernet) and equalization techniques, RF and millimeter-wave front-ends, power management ICs and voltage regulators, memory interface and readout circuits (including NAND/DRAM/CXL), neuromorphic and in-memory computing hardware, AI accelerator circuits, biomedical and sensor interface electronics, and design methodologies and EDA techniques that enable low-power and high-efficiency implementation.

Guest Editors

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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

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