Special Issue

FPGA-Based Accelerators of Deep Learning and Neuromorphic Computing

Message from the Guest Editor

This Special Issue will cover advanced techniques for the hardware acceleration of deep learning and neuromorphic algorithms on FPGA, ranging from micro-architecture design to automatic compilation, as well as hardware-friendly algorithm optimization, including the latest ongoing research efforts in these fields but not limited to:

- Algorithm–hardware co-design for FPGA-based intelligent acceleration;
- System and software for FPGA accelerator compilation;
- Reconfigurable/adaptive computing for AI/ML;
- FPGA-based rapid prototyping of AI/ML system;
- Programmable neuromorphic computing architectures on FPGAs;
- Implementation of novel intelligent applications on FPGAs;
- AI/ML systems based on coarse-grained reconfigurable architectures (CGRAs);
- Implementation and evaluation of spiking neural networks on FPGAs.

Guest Editor

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

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