Special Issue

Machine Learning in Networking Systems and Applications

Message from the Guest Editors

This Special Issue of Electronics, entitled "Machine Learning in Networking Systems and Applications", aims to delve into the dynamic interplay between machine learning (ML) and networking systems. The inherently complex nature of networking environments, characterized by multiple layers, numerous protocols, and extensive data interactions, makes them particularly amenable to ML applications. Machine learning introduces powerful data-driven methods to these intricate systems, significantly enhancing their efficiency and ability to manage the vast amounts of data they process daily. In this Special Issue, we invite research that addresses how ML can enhance networking operations, how networks can support the functionality and effectiveness of ML technologies, or both. Contributors are encouraged to focus on any single aspect of this interaction, presenting innovative research or practical applications that demonstrate the enhancement of ML integrated with networking systems. Our goal is to showcase impactful research that addresses the challenges and solutions at the intersection of ML and networking.

Guest Editors

Dr. Peizhong Ju

Department of Computer Science, University of Kentucky, Lexington, KY 40506, USA

Dr. Chengzhang Li

AI-EDGE Institute, Ohio State University, Columbus, OH 43212, USA

Deadline for manuscript submissions

15 October 2025



Electronics

an Open Access Journal by MDPI

Impact Factor 2.6 CiteScore 6.1



mdpi.com/si/217260

Electronics
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
electronics@mdpi.com

mdpi.com/journal/electronics





Electronics

an Open Access Journal by MDPI

Impact Factor 2.6 CiteScore 6.1



About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and Telecommunications, Politecnico di Torino, 10129 Torino, Italy

Author Benefits

High Visibility:

indexed within Scopus, SCIE (Web of Science), CAPlus / SciFinder, Inspec, Ei Compendex and other databases.

Journal Rank:

JCR - Q2 (Engineering, Electrical and Electronic) / CiteScore - Q1 (Electrical and Electronic Engineering)

Rapid Publication:

manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).

