Special Issue

Emerging Technologies of High-Performance and Parallel Computing

Message from the Guest Editor

The end of the Moore's law and the need for energy-efficiency solutions have brought new challenges to continue increasing performance. New architectures, such as heterogeneous cores, deep memory hierarchies, near memory processing, among many others, have emerged as a possible solution to address such important challenges. In a more and more complex upcoming world, the efforts for a new software stack is more important and necessary than ever. This Special Issue aims to promote any research targeting potential solutions to the challenges above presented at any level of the computing stack, being particularly important power-efficiency architectures targeting performance portability and/or performance optimization. Papers are being sought on many aspects of emerging computing including:

- Emerging solutions for High Performance Computing Applications and Problems;
- Emerging Parallel Programming Paradigms for Performance Portability and/or Performance Optimization;
- Reliability/Benchmarking/Measurements on Emerging Platforms;
- Emerging Computer Architectures;
- Standardization on Emerging Platforms;

Guest Editor

Dr. Pedro Valero-Lara

Oak Ridge National Laboratory, Oak Ridge, TN 37830, USA

Deadline for manuscript submissions

closed (31 May 2022)

Electronics

About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero
Department of Electronics and Telecommunications, Politecnico di Torino, 10129 Torino, Italy

Author Benefits

High Visibility:

indexed within Scopus, SCIE (Web of Science), CAPlus / SciFinder, Inspec, Ei Compendex and other databases.

Journal Rank:

JCR - Q2 (Engineering, Electrical and Electronic) / CiteScore - Q1 (Electrical and Electronic Engineering)

Rapid Publication:

manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).