# **Special Issue**

## **Hardware and Architecture**

### Message from the Guest Editor

Novel trends in Cyber physical Systems, the Internet of things, and also neuronal networks and machine learning, need new hardware and architecture. The goal is to reduce energy consumption more, and to simultaneously increase performance. This Special Issue will cover novel trends, research, and the development of hardware and architecture, and contributes to the community with different views and solutions. **Keywords:** 

- Embedded system
- Low power
- Reconfigurable computing

Prof. Dr. Michael Huebner

### **Guest Editor**

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### Deadline for manuscript submissions

closed (30 September 2018)



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### Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

#### Editor-in-Chief

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