Special Issue

Hardware Architectures for Real Time Image Processing

Message from the Guest Editors

The development of hardware architectures for real-time image processing is an important requirement when dealing with critical applications where data must be processed in a restricted timeframe. Moreover, it is an interdisciplinary field that gives support to a wide variety of disciplines, such as electronic engineering, computer science, industrial control, physics, mathematics, biology, food quality assessment, medicine, etc. Although these circumstances arise in several applications, and several solutions have been proposed in the past, the large amount of data per unit of time currently provided by image sensors supposes a major challenge for feasible hardware implementations. Not only have the frame-rates and the resolution of the images increased, but additionally, some image modalities incorporate spectral information, depth information, contextual information, metadata, etc., making the implementation requirements even more stringent. In addition to time processing, many systems must meet constraints on weight, size, power or cost, resulting in exhaustive and time-prohibitive design-space explorations.

Guest Editors

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Deadline for manuscript submissions

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Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

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