Special Issue

Advanced AI Hardware Designs Based on FPGAs

Message from the Guest Editor

In this Special Issue, we invite the latest developments in the field of advanced AI hardware design based on FPGA, which can show the device's strengths, such as hardware/software co-design, customization, and scalability over other types of hardware. Topics include but are not limited to:

- DNN inference/training accelerators on FPGAs;
- Multi-FPGA approaches for scalable ML acceleration;
- Distributed deep learning architecture on multiple FPGAs;
- Hardware/Software co-design for energy-efficient ML on FPGA;
- Narrow-precision and efficient floating-point representation on FPGA for ML applications;
- Design automation from the ML algorithm to FPGAs;
- Soft DNN processor to cover a wide range of ML applications.

Welcome to contribute!

Guest Editor

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

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