Special Issue

Fault-Tolerant Architectures and Applications for Embedded and Reconfigurable Systems-on-a-Chip

Message from the Guest Editors

With the increasing dependability of computing system in everyday life, such as in automotive and industrial industries, scientific experiments, and space missions, it is important to have systems that guarantee reliability and are capable of operating, even in harsh conditions, as reliability has a direct impact on a systems's performance. The aim of this Special Issue is to gather the most recent developments and applications of embedded and reconfigurable systems, covering, but not limited to, the following scopes:

- Applications of fault-tolerance mechanisms in embedded and reconfigurable (SoC-FPGA) systems, e.g., neural networks, edge computing, and automotive and space systems
- Approximating computing architectures
- Fault-tolerance methods for heterogeneous systems
- Communication mechanisms for inter-layer fault-tolerance stack
- Unified hardware–software fault-tolerance
- Systems with limited resources: low-power and portable systems
- Open source implementations
- EDA tools.

Welcome to contribute.

Guest Editors

Dr. Rui Policarpo Duarte
INESC-ID, Instituto Superior Técnico, University of Lisbon, Lisbon, Portugal

Dr. Paulo Flores
INESC-ID, Instituto Superior Técnico (ECE Department), University of Lisbon, Lisbon, Portugal

Deadline for manuscript submissions

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Electronics
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
electronics@mdpi.com

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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero
Department of Electronics and Telecommunications, Politecnico di Torino, 10129 Torino, Italy

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