Special Issue

Fault-Tolerant Digital Circuits: Protection Techniques, CAD Tools and Emerging Applications

Message from the Guest Editor

Reliability is a growing concern for electronic digital circuits. In this Special Issue, we are interested in exploring new protection techniques for electronic systems, CAD tools to perform reliability tests and novel applications, in different fields, which can benefit from the use of fault-tolerant circuits. These topics include but not limited to:

- New protection techniques for digital circuits
- Fault-tolerant computer architectures
- Error correction codes (ECC) to increase reliability in innovative ways
- Signal and image processing systems with a focus on reliability
- CAD tools and methods to emulate error injection test processes
- Novel applications of fault tolerant digital circuits
 Welcome to contribute.

Guest Editor

Prof. Dr. Juan Antonio Maestro ARIES Research Center, Universidad Nebrija, 28040 Madrid, Spain

Deadline for manuscript submissions

closed (31 March 2021)



Electronics

an Open Access Journal by MDPI

Impact Factor 2.6 CiteScore 6.1



mdpi.com/si/31266

Electronics
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
electronics@mdpi.com

mdpi.com/journal/electronics





Electronics

an Open Access Journal by MDPI

Impact Factor 2.6 CiteScore 6.1



About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and Telecommunications, Politecnico di Torino, 10129 Torino, Italy

Author Benefits

High Visibility:

indexed within Scopus, SCIE (Web of Science), CAPlus / SciFinder, Inspec, Ei Compendex and other databases.

Journal Rank:

JCR - Q2 (Engineering, Electrical and Electronic) / CiteScore - Q1 (Electrical and Electronic Engineering)

Rapid Publication:

manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).

