

Special Issue

Recent Advances in Field-Programmable Logic and Applications

Message from the Guest Editor

Field-programmable logic enables new tradeoffs between performance, power efficiency, security, flexibility and dependability. Simultaneously, device architectures and development tools have been evolving to meet the new challenges. This special issue will bring together high-quality contributions that highlight novel approaches and recent breakthroughs in all areas related to programmable logic (technology, architectures, EDA tools and methodologies) as well as emerging application domains ranging from embedded systems up to data centers. The topics covered by the Special Issue include, but are not limited to, the following:

- Reconfigurable computing technology
- FPGA/PMPSoC architectures
- Hardware accelerators
- Heterogeneous reconfigurable platform architectures
- CAD and High-level synthesis tools and methodologies for reconfigurable platforms
- Dynamic reconfiguration
- Domain-specific reconfigurable computing
- Programmable logic in the data center and big data analytics
- Programmable logic for machine learning in embedded and HPC systems
- Security of reconfigurable platforms

Guest Editor

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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

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