

Special Issue

Energy Aware HPC Revisited: AI Concerns, Architectures and Optimization Strategies

Message from the Guest Editors

This Special Issue is focused on sharing and showing new proposals regarding energy aware HPC optimization strategies in the context of novel applications supporting AI. Prospective authors are invited to submit high-quality original contributions and reviews to this Special Issue. Potential topics include, but are not limited to, the following:

- Cost efficient HPC architectures, topologies, and strategies
- Energy aware HPC algorithm and architecture adequacy
- Approximate computing
- Low-power reconfigurable accelerators for heterogeneous HPC
- Configurable, custom, optimized CNNs topologies for energy aware HPC

Please click [here](#) to find information!

Welcome to contribute!

Guest Editors

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Deadline for manuscript submissions

closed (30 September 2022)



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Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

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