# Special Issue

# Embedded and Integrated Circuits and Systems in Real Engineering Applications

## Message from the Guest Editors

Embedded systems, such as FPGA, FPAA, Raspberry Pi, and microcontrollers, have demonstrated their usefulness in the development of applications in a wide variety of engineering topics, including software-defined radio, digital filters, neuromorphic networks, secure communications, control and synchronization, image processing, and so on. Some of these applications are required in the IoT and artificial intelligence for IoT, so designing integrated circuits faces the challenge of providing very low voltage and low power consumption for wireless systems. This Special Issue welcomes work related to the design of circuits and systems for signal-processing applications.

- Devices, circuits, and systems designed with modern integrated circuit technologies;
- Theory on devices, circuits, and systems for modern applications;
- Modeling, simulation, and optimization of circuits and systems;
- Embedded/hybrid hardware for IoT and neuromorphic systems:
- Integrated circuits and systems for biomedical and industrial applications.
- Artificial intelligence in circuits and systems for engineering applications;
- Speech/video signal-processing circuits and systems;

## **Guest Editors**

Prof. Dr. Esteban Tlelo-Cuautle

Dr. Everardo Inzunza-González

Dr. Walter Daniel Leon-Salas

## Deadline for manuscript submissions

closed (31 July 2023)



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## About the Journal

## Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

### Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and Telecommunications, Politecnico di Torino, 10129 Torino, Italy

## **Author Benefits**

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## **Rapid Publication:**

manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.4 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the second half of 2024).

