

Special Issue

Error-Control Coding Algorithms and Architectures for Modern Applications

Message from the Guest Editors

The development of algorithms and architectures for error-control coding (ECC) has been a rapidly growing field for several decades now, allowing the design of reliable data storage systems, data centers, and communication networks. Although great improvements have been achieved by the community with capacity-approaching codes and efficient implementations of gigabit decoders, more challenging requirements and constraints come with the release of new standards, the scaling of manufacturing processes, the appearance of new memory technologies, and the quantum-computing revolution. This Special Issue on “Error-Control Coding Algorithms and Architectures for Modern Applications” aims to include different implementation solutions and design of algorithms and error-correction codes, provided that such solutions suppose a significant scientific contribution in the field.

Guest Editors

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

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