Special Issue

Efficient Algorithms and Architectures for DSP Applications

Message from the Guest Editors

Optimization of the implementation of DSP algorithms and architectures is an essential part of research and design for many modern applications. Optimizing such computationally intensive applications is a challenging issue that requires a clever design or restructuring of the algorithm or architecture. This Special Issue focuses on papers that demonstrate how these design challenges can be overcome using innovative solutions. Topics of interest for this Special Issue include but are not limited to:

- VLSI signal processing;
- Signal processing methods for an efficient implementation;
- Optimization of the VLSI implementation of multimedia blocks;
- Low-power circuits and systems for DSP applications;
- Efficient adaptive/learning algorithms (low complexity/fast versions, optimized parameters, etc.);
- Tensor-based signal processing (efficient decomposition methods, low-rank approximations, etc.);
- Sparsity-aware algorithms.

https://www.mdpi.com/journal/electronics/special_issues/DSP_Applications Welcomed to contribute!

Guest Editors

Prof. Dr. Doru Florin Chiper

Faculty of Electronics, Telecommunications and Information Technology, Gheorghe Asachi Technical University of Iaşi, 700506 Iaşi, Romania

Prof. Dr. Constantin Paleologu

Department of Telecommunications, University Politehnica of Bucharest, 1-3, Iuliu Maniu Blvd., 061071 Bucharest, Romania

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Electronics
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
electronics@mdpi.com

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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero
Department of Electronics and Telecommunications, Politecnico di Torino, 10129 Torino, Italy

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manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).

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