Special Issue

Reconfigurable Computing – Hardware/Software Co-design

Message from the Guest Editors

This Special Issue aims at discussing the efficient development of reconfigurable hardware/software systems, the parameters that need to be taken into account, the appropriate technologies, programming languages and compilers. The main aim of this Special Issue is to seek high-quality submissions that present and discuss the recent advances in the development of reconfigurable hardware/software systems, especially focusing on application successes. The topics of interest include, but are not limited to, the following:

- Co-design methods, tools, and compilers for reconfigurable hardware/software systems
- Algorithms implemented on reconfigurable hardware/sowtware
- Reconfigurable computing applications

Education for reconfigurable hardware/software systems including courses, teaching and training experience, lab equipment, design and applications

Guest Editors

Dr. Iouliia Skliarova

Department of Electronics, Telecommunications and Informatics, IEETA, University of Aveiro, 3810-193 Aveiro, Portugal

Prof. Dr. Mikhail Lavrentiev

 Sobolev Institute of Mathematics SB RAS, Novosibirsk, Russia
 Institute of Automation and Electrometry SB RAS, Faculty of Information Technologies, Novosibirsk State University, 630090 Novosibirsk, Russia

Deadline for manuscript submissions

closed (30 June 2023)



Electronics

an Open Access Journal by MDPI

Impact Factor 2.6 CiteScore 6.1



mdpi.com/si/108245

Electronics
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
electronics@mdpi.com

mdpi.com/journal/electronics





Electronics

an Open Access Journal by MDPI

Impact Factor 2.6 CiteScore 6.1



About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and Telecommunications, Politecnico di Torino, 10129 Torino, Italy

Author Benefits

High Visibility:

indexed within Scopus, SCIE (Web of Science), CAPlus / SciFinder, Inspec, Ei Compendex and other databases.

Journal Rank:

JCR - Q2 (Physics, Applied) / CiteScore - Q1 (Electrical and Electronic Engineering)

Rapid Publication:

manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.4 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the second half of 2024).

