Special Issue

Analog/RF Integrated Circuits and System Design for Neural Interface Applications

Message from the Guest Editor

The topics of interest for this Special Issue include the design and implementation of neural interface SoCs, as well as the works related to individual analog and RF IC design building blocks for BMI applications using CMOS technologies. **Keywords:**

- Brain–machine interface (BMI)
- Bidirectional neural interface system-on-chip (SoC)
- Analog front-end
- Neural recording
- Low-noise neural amplifier
- Low-power analog-to-digital converter (ADC)
- Neural stimulation
- Wireless power conversion and management
- Low-power wireless transceiver
- Stimulation artifact
- Charge balancing
- Action potential (AP) and local field potential (LFP)
- Electrocorticography (ECoG)
- Electroencephalography (EEG)

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Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

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