# **Special Issue**

# Analog/RF Integrated Circuits and System Design for Neural Interface Applications

## Message from the Guest Editor

The topics of interest for this Special Issue include the design and implementation of neural interface SoCs, as well as the works related to individual analog and RF IC design building blocks for BMI applications using CMOS technologies. **Keywords:** 

- Brain-machine interface (BMI)
- Bidirectional neural interface system-on-chip (SoC)
- Analog front-end
- Neural recording
- Low-noise neural amplifier
- Low-power analog-to-digital converter (ADC)
- Neural stimulation
- Wireless power conversion and management
- Low-power wireless transceiver
- Stimulation artifact
- Charge balancing
- Action potential (AP) and local field potential (LFP)
- Electrocorticography (ECoG)
- Electroencephalography (EEG)

## Guest Editor

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# Deadline for manuscript submissions

closed (31 October 2021)



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### Message from the Editor-in-Chief

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### Editor-in-Chief

Prof. Dr. Flavio Canavero Department of Electronics and Telecommunications, Politecnico di Torino, 10129 Torino, Italy

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