

Special Issue

Advanced CMOS Devices and Applications

Message from the Guest Editors

We are pleased to invite you to submit your abstracts in the fields related to advanced CMOS transistors and emerging non-volatile memories, including materials fundamentals, process technologies, device physics, novel device architectures, and neuromorphic computing applications. Papers related to device and material technologies for advanced CMOS and emerging non-volatile memories for neuromorphic computing applications are solicited, including the following: (1) Advanced CMOS device architectures for high performance, ultralow-power consumption, and reliability improvement; (2) Emerging materials and advanced process technologies for high-mobility channel, gate stack formation, S/D contact, and junction; (3) Device physics, novel characterization methods, TCAD simulation, and ab initio calculation for advanced CMOS and emerging non-volatile memories; (4) Conventional memories and emerging memories such as ReRAM, MRAM, PCRAM, and FeRAM; (5) Memory device physics, reliability, and modeling; (6) Synaptic devices for neuromorphic computing applications.

Guest Editors

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Deadline for manuscript submissions

closed (28 February 2023)



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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

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