Special Issue

CMOS Integrated Circuits Design

Message from the Guest Editors

CMOS integrated circuits have been an enabling technology for the modern information age. The increase in both transistor density and performances, driven by Moore's law, has been the leading factor in the technological advances of today's complex mixed-signal systems. CMOS technology exhibits advantages for both digital and analog circuits in terms of reduced dimensions of transistors, higher working frequencies, and lower fabrication costs. However, the increasing circuit complexity of scaled CMOS technologies comes with many design challenges. On the digital side, although both static and dynamic power decrease for a single logic gate, the higher speed of circuits leads to signal integrity issues. Technology scaling is detrimental to the power consumption of analog circuits. Indeed, in order to achieve a target dynamic range with a decreased supply voltage, higher bias currents must be used to counteract the thermal noise contribution. This Special Issue aims to collect original research articles of recent advances in CMOS integrated circuits design in scaled technologies.

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Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

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