

Special Issue

Evolutionary Hardware-Software Codesign Based on FPGA

Message from the Guest Editors

This Special Issue, entitled “Evolutionary Hardware-Software Codesign Based on FPGA”, is intended to present the latest advances in applications, tools, and designs benefiting from this hybrid technology, including (but not limited to):

- Hardware–software codesign for FPGA-based acceleration;
- System and software for FPGA accelerator compilation;
- High-level abstractions and tools for FPGAs;
- AI and ML application based on FPGA implementations;
- RISC-V-embedded cores acceleration;
- Programmable neuromorphic computing architectures using FPGAs;
- Implementation of novel applications based on FPGAs;
- FPGA Implementation of deep neural networks.

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

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