Special Issue

Architecture and CAD for Field-Programmable Gate Arrays (FPGAs)

Message from the Guest Editor

Field-programmable gate array technology has advanced significantly, and FPGAs represent a substantial portion of overall semiconductor growth. Advances in the device architecture and supporting CAD tools have made FPGAs a very viable technology for implementing very large-scale high-performance systems. Recent progress in compilation technology has allowed easy translation of complex high-level software abstractions into efficient hardware implementations. FPGA based custom-computing systems are becoming especially important for domain-specific architectures due to their high performance-to-energy ratio. This Special Issue will address advances from FPGA devices to FPGA systems and bring a compilation of recent research results in the areas listed below.

- FPGA device architecture
- CAD for FPGAs
- Compilations tools for FPGAs
- FPGA-based computing systems
- FPGA applications

Guest Editor

Prof. Dr. Dinesh Bhatia

Department of Electrical and Computer Engineering, University of Texas at Dallas, Richardson, TX 75083, USA

Deadline for manuscript submissions

closed (31 December 2021)

Electronics

an Open Access Journal
by MDPI

Impact Factor 2.6
CiteScore 6.1

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electronics

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero
Department of Electronics and Telecommunications, Politecnico di Torino, 10129 Torino, Italy

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