

Special Issue

Intelligent Systems Design and Engineering Applications

Message from the Guest Editors

The Special Issue on "Intelligent Systems Design and Engineering Applications" for the International Conference on Intelligent Systems Design and Engineering Applications (ISDEA) (<https://www.isdea.org/>) aims to explore cutting-edge research and practical developments in this critical field. This issue will serve as a platform for researchers, engineers, and practitioners to share their latest findings, methodologies, and applications in the design and implementation of intelligent systems. The ISDEA conference provides an ideal forum for discussing the latest advancements in this rapidly evolving domain. This Special Issue will primarily feature extended versions of selected papers presented at the ISDEA conference. However, we also welcome high-quality submissions from researchers and practitioners who have not presented at the conference but are working on relevant topics.

Guest Editors

Prof. Dr. Shiho Kim

School of Integrated Technology (SIT), Yonsei University, Incheon 21983, Republic of Korea

Dr. Yagiz Nalcakan

School of Integrated Technology (SIT), Yonsei University, Incheon 21983, Republic of Korea

Deadline for manuscript submissions

31 August 2025



Electronics

an Open Access Journal
by MDPI

Impact Factor 2.6
CiteScore 6.1



mdpi.com/si/216176

Electronics
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
electronics@mdpi.com

[mdpi.com/journal/
electronics](https://mdpi.com/journal/electronics)





Electronics

an Open Access Journal
by MDPI

Impact Factor 2.6
CiteScore 6.1



[mdpi.com/journal/
electronics](https://mdpi.com/journal/electronics)



About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and Telecommunications, Politecnico di
Torino, 10129 Torino, Italy

Author Benefits

High Visibility:

indexed within Scopus, SCIE (Web of Science), CAPlus /
SciFinder, Inspec, Ei Compendex and other databases.

Journal Rank:

JCR - Q2 (Engineering, Electrical and Electronic) /
CiteScore - Q1 (Electrical and Electronic Engineering)

Rapid Publication:

manuscripts are peer-reviewed and a first decision is
provided to authors approximately 16.8 days after
submission; acceptance to publication is undertaken in 2.4
days (median values for papers published in this journal in
the first half of 2025).