Special Issue

Safeguarding Systems: Approaches to Resolving Hardware Security Challenges

Message from the Guest Editors

Hardware security focuses on research related to integrated circuits and corresponding hardware with physical and logical threats. There exist common security challenges, including physical attacks, supply chain vulnerabilities, firmware flaws, architecture imperfections, etc. There are still emerging security and privacy threats, e.g., blockchain security, cryptocurrency security, privacy-enhancing architecture, etc. This Special Issue aims to facilitate the rapid growth of hardware security research and development and also focuses on academic and industrial research on all topics related to hardware security and trust. Topics of interest to this Special Issue include, but are not limited to, the following:

- Security analysis engines;
- Security-aware CAD tools;
- VLSI verification for security and trust;
- Automatic side-channel vulnerability assessment;
- Security equivalence checking;
- Formal method-based security verification;
- Automatic hardware vulnerability mining method;
- New hardware vulnerability or attack:
- Hardware-assisted computer security;
- Secure hardware design;
- Al-based hardware security analysis.

Guest Editors

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Deadline for manuscript submissions

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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

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manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).

