

## Special Issue

# Radiation Tolerant Electronics, Volume III

### Message from the Guest Editors

Research on radiation-tolerant electronics has increased rapidly over the last few years, resulting in many interesting approaches to model radiation effects and design radiation-hardened integrated circuits and embedded systems. The main aim of this Special Issue is to seek high-quality submissions that highlight emerging applications, address recent breakthroughs in modeling radiation effects in advanced electronic devices and circuits, the design of radiation-hardened analog, mixed-signal, RF and digital integrated circuits and radiation hardness testing methodologies. The topics of interest include, but are not limited to:

- Basic mechanisms of radiation effects in electronic devices
- Compact modeling of radiation effects and circuit/layout level optimization (TID and SEE)
- Radiation effects in power devices/circuits
- Design of radiation-hardened integrated circuits (analog/RF/mixed-signal/digital)
- Radiation hardening and fault tolerance in FPGAs
- Radiation hardness assurance testing

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### Guest Editors

Prof. Dr. Paul Leroux

Department of Electrical Engineering (ESAT), KU Leuven,  
Kleinhoefstraat 4, 2440 Geel, Belgium

Dr. Jeffrey Prinzie

Assistance Professor, Department of Electrical Engineering, Katholieke  
Universiteit (KU) Leuven, 3000 Leuven, Belgium

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### Deadline for manuscript submissions

closed (29 February 2024)



## Electronics

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*Electronics*  
Editorial Office  
MDPI, Grosspeteranlage 5  
4052 Basel, Switzerland  
Tel: +41 61 683 77 34  
[electronics@mdpi.com](mailto:electronics@mdpi.com)

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## About the Journal

### Message from the Editor-in-Chief

*Electronics* is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

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### Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and Telecommunications, Politecnico di Torino, 10129 Torino, Italy

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