Special Issue

Heterogeneous and Energy-Efficient Computing Systems

Message from the Guest Editor

The topics of interest of this Special Issue (SI) include, but are not limited to, the following:

- Evaluating and modeling the energy efficiency of emerging computation workloads on new heterogeneous architectures. This involves assessing and quantifying the energy consumption of diverse computational tasks running on these novel architectures, enabling researchers to gain insights into the energy characteristics and requirements of different workloads.
- Understanding the trade-off between energy efficiency and other crucial aspects of computing, such as reliability and performance. For example, this can include exploring the intricate relationship between energy efficiency and these key factors to comprehend the potential trade-offs and synergies. This knowledge will guide the development of strategies that optimize energy efficiency while maintaining acceptable levels of reliability and performance.

Developing effective solutions for achieving high energy efficiency in heterogeneous computing systems. This encompasses various approaches, including algorithms and application-level techniques, system-level optimizations, and software-hardware co-design strategies.

Guest Editor

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Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

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