Special Issue

Computer-Aided Design for Hardware Security and Trust

Message from the Guest Editors

Despite the significant attention paid to hardware security over the past decade, there remains a need for a more mature set of security-aware tools to help chip designers verify security vulnerabilities automatically and effectively across all levels of abstraction throughout the chip design process. To address this need, we aim to leverage Computer-Aided Design (CAD) tools and commercial design tools for security and trust to ensure the reliability of the chip. This Special Issue will focus on exploring the latest academic and industrial research on all aspects of CAD for hardware security and trust. Topics of interest to this Special Issue include, but are not limited to:

- Security analysis engines;
- Security-aware CAD tools;
- VLSI verification for security and trust;
- Automatic side-channel vulnerability assessment;
- Security equivalence checking;
- Formal method-based security verification.

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Deadline for manuscript submissions

closed (30 June 2024)

Electronics

an Open Access Journal by MDPI

Impact Factor 2.6
CiteScore 6.1

mdpi.com/si/163458

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

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