Special Issue

Advances in High-Performance and Parallel Computing

Message from the Guest Editors

High-Performance Computing (HPC) and Parallel Computing play a vital role in addressing the growing demands of modern science, engineering, and data-driven applications. HPC and Parallel Computing enable researchers and practitioners to leverage multi-core processors, distributed systems, and accelerators such as GPUs and NPUs to solve problems involving increasing complexity and datasets. We hope this Special Issue provides valuable insights, fosters collaboration, and inspires future innovation in this dynamic and essential field. Research areas may include (but are not limited to) the following:

- Scalable and Distributed Computing;
- High-Performance Architectures and Systems;
- GPUs, Accelerators, and Heterogeneous Architectures for HPC;
- Programming Models and Languages for Parallel Computing;
- Compilers and Runtime Systems for Parallel Computing;
- Machine Learning and AI for HPC (and vice versa);
- Parallel Algorithms and Data Structures;
- Energy Efficiency and Green Computing in HPC;
- Fault Tolerance and Resilience in HPC;
- Performance Modeling, Benchmarking, and Tuning.

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

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