# **Special Issue**

## Progress and Future Development of Real-Time Systems on Chip

## Message from the Guest Editor

We are delighted to announce the forthcoming Special Issue on "Progress and Future Development of Real-Time Systems on Chip". This Special Issue is focused on topics relevant to real-time systems on chip and related areas, including but not limited to:

- Systems on Chip (SoC): Novel design methodologies, architectures, and verification techniques.
- ASIC and FPGA Designs: Cutting-edge research on application-specific integrated circuits (ASICs) and field-programmable gate arrays (FPGAs).
- CPU Architectures: Innovations in CPU microarchitecture and instruction set design to enhance real-time processing capabilities, exploit parallelism, and improve predictability.
- Real-Time Task Scheduling: State-of-the-art algorithms, techniques, and frameworks for real-time task scheduling in multi-core and heterogeneous systems, ensuring timely execution and meeting stringent deadlines.
- Data Sorting Solutions: Efficient algorithms and hardware implementations for real-time data sorting in big data applications, high-performance computing, and streaming data scenarios.

## Guest Editor

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### Deadline for manuscript submissions

closed (15 June 2025)



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## About the Journal

## Message from the Editor-in-Chief

*Electronics* is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

## Editor-in-Chief

Prof. Dr. Flavio Canavero Department of Electronics and Telecommunications, Politecnico di Torino, 10129 Torino, Italy

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