Special Issue

Low Power Analog and Mixed-Signal Circuits for Edge Computing

Message from the Guest Editor

In recent decades, the emergence of Internet-of-Things (IoT) and smart sensors applications have necessitated the development of energy efficient and highperformance edge sensing/computing circuits. A battery-powered IoT sensor node typically consists of power management circuits, analog front-end, data converters, computing macro., etc. Given the requirement of less power and relatively low precision, it is feasible to consider analog and mixed-signal computing for low-SNR applications, which can be more efficient than digital methods. Thus, mixed-signal computing methods have been garnering increasing attention. This Special Issue aims to publish the latest findings, new research developments, and future trends and innovations in the design, estimation, control, and optimization of low-power analog and mixed-signal integrated circuits, and their applications in the Internetof-Things devices. We welcome theoretical and experimental studies, as well as review papers.

Guest Editor

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

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