Special Issue

Advanced Technologies for Intelligent Vehicular Networks

Message from the Guest Editors

With the rapid evolution of communication and computation technologies, intelligent vehicular networks have emerged as a key enabler for enhanced traffic management, road safety, and autonomous driving. The integration of 6G technologies, Al. edge computing, and the blockchain is enabling intelligent vehicular networks with real-time, ultra-reliable, low-latency, and highthroughput communications. Despite these advancements, several challenges remain, including security vulnerabilities, intermittent connectivity, and efficient data management in highly dynamic vehicular environments. This Special Issue focuses on innovative solutions and emerging trends, addressing the design, optimization, and implementation of advanced technologies for intelligent vehicular networks. Topics of interest include, but are not limited to, novel network architectures. Al-driven mobility models, vehicular cloud and edge computing, secure communication protocols, and efficient routing mechanisms for real-time V2X communication. Additionally, contributions exploring the role of SDN, the blockchain, game theory, and deep learning for vehicular network optimization are welcomed.

Guest Editors

Dr. Sang Ik Han

School of Smart IT, Semyung University, Jecheon 27136, Chungcheongbuk-do, Republic of Korea

Dr. Kiho Lim

Department of Computer Science, William Paterson University, Wayne, NJ 07470, USA

Deadline for manuscript submissions

15 April 2026



Electronics

an Open Access Journal by MDPI

Impact Factor 2.6 CiteScore 6.1



mdpi.com/si/234365

Electronics
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
electronics@mdpi.com

mdpi.com/journal/electronics





Electronics

an Open Access Journal by MDPI

Impact Factor 2.6 CiteScore 6.1



About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and Telecommunications, Politecnico di Torino, 10129 Torino, Italy

Author Benefits

High Visibility:

indexed within Scopus, SCIE (Web of Science), CAPlus / SciFinder, Inspec, Ei Compendex and other databases.

Journal Rank:

JCR - Q2 (Engineering, Electrical and Electronic) / CiteScore - Q1 (Electrical and Electronic Engineering)

Rapid Publication:

manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).

