

Special Issue

Design of Low-Power Circuits and Systems

Message from the Guest Editors

In today's technology-oriented world, the demand for low-power, high-speed, miniaturized, and reliable integrated circuits (ICs) is ever increasing. We aim to collect contributions pertaining to low-power circuits and systems design, and the topics to be covered include, but are not limited to, the following:

- Power-aware digital, analog, or hybridized circuit design;
- Low-voltage and near-threshold computing;
- Energy-efficient computing approaches for IoT applications;
- Power-efficient AI/ML hardware and accelerator approaches;
- Energy harvesting circuits for self-powered systems;
- Low-power memory system designs;
- Beyond CMOS devices for low-power and/or non-volatile applications;
- System-on-chip (SoC) power management techniques;
- Low-power sensor interfaces for wearable and implantable devices.

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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

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