

Special Issue

5G/6G IoT: Smart RF Link Integrated Circuit System Design

Message from the Guest Editor

5G/6G heterogeneous computing and communication systems will be one of the most important technologies for autonomous vehicles and smart mobility systems. To enable those systems, innovative 5G/6G RF IC and system-on-chip (SoC), and application-specific integrated circuits (ASIC) designs will be needed to improve far beyond mobile communication links by high-performance RF integrated transceivers, transmitters, and receivers design. This Special Issue focuses on the analysis, design, and implementation of RF/analog integrated circuit design for energy-efficient wireless/wireline communication, RF/THz electronics for sensing/imaging, biomedical applications, etc.

- 5G/6G heterogeneous computing and communication system
- High-performance RF integrated transceivers
- High-performance transmitters and receivers
- Analog integrated circuit design for wireless/wireline communication
- RF/THz electronics for sensing/imaging
- biomedical applications.

Guest Editor

Prof. Dr. Gyung-Su Byun
College of Engineering, Inha University, Incheon 22212, Korea

Deadline for manuscript submissions

closed (30 June 2021)



Electronics

an Open Access Journal
by MDPI

Impact Factor 2.6
CiteScore 6.1



mdpi.com/si/40729

Electronics
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
electronics@mdpi.com

[mdpi.com/journal/
electronics](https://mdpi.com/journal/electronics)





Electronics

an Open Access Journal
by MDPI

Impact Factor 2.6
CiteScore 6.1



[mdpi.com/journal/
electronics](https://mdpi.com/journal/electronics)



About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and Telecommunications, Politecnico di
Torino, 10129 Torino, Italy

Author Benefits

High Visibility:

indexed within Scopus, SCIE (Web of Science), CAPlus /
SciFinder, Inspec, Ei Compendex and other databases.

Journal Rank:

JCR - Q2 (Engineering, Electrical and Electronic) /
CiteScore - Q1 (Electrical and Electronic Engineering)

Rapid Publication:

manuscripts are peer-reviewed and a first decision is
provided to authors approximately 16.4 days after
submission; acceptance to publication is undertaken in 2.4
days (median values for papers published in this journal in
the second half of 2024).