Special Issue

5G/6G IoT: Smart RF Link Integrated Circuit System Design

Message from the Guest Editor

5G/6G heterogeneous computing and communication systems will be one of the most important technologies for autonomous vehicles and smart mobility systems. To enable those systems, innovative 5G/6G RF IC and system-on-chip (SoC), and application-specific integrated circuits (ASIC) designs will be needed to improve far beyond mobile communication links by high-performance RF integrated transceivers, transmitters, and receivers design. This Special Issue focuses on the analysis, design, and implementation of RF/analog integrated circuit design for energy-efficient wireless/wireline communication, RF/THz electronics for sensing/imaging, biomedical applications, etc.

- 5G/6G heterogeneous computing and communication system
- High-performance RF integrated transceivers
- High-performance transmitters and receivers
- Analog integrated circuit design for wireless/wireline communication
- RF/THz electronics for sensing/imaging
- biomedical applications.

Guest Editor

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

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