Special Issue

Low Power Architectures for Digital Video and Image Compression

Message from the Guest Editors

Compression of static or dynamic image data is one of the most crucial topics in image processing, digital imaging, computer vision, pattern recognition, artificial intelligence, etc. In particular, digital images (including video images) require high computational complexities with the popularity of using deep neural network models. Therefore, low power consumption from low computational complexity is one of the most practical issues in industrial fields. Thus, advanced architecture design for image and video processing is required from software and hardware perspectives. This Special Issue chiefly deals with innovative solutions for image and video compressions, including efficient architecture design with efficient algorithm development. Authors are encouraged to submit high-quality and up-to-date research contributions.

Guest Editors

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Deadline for manuscript submissions

closed (31 March 2024)

Electronics

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Impact Factor 2.6
CiteScore 6.1

mdpi.com/si/153294

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Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

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