Special Issue

Hardware/Algorithm Co-Design for Communication and Networking Acceleration

Message from the Guest Editor

This Special Issue of Electronics aims to bring together cutting-edge research in hardware/algorithm co-design for communication and networking acceleration. It seeks to explore innovative methodologies, architectures, and technologies that address the current limitations and open up new possibilities for future systems. By fostering a deeper understanding of how hardware and software can be synergistically combined, this issue will contribute to the development of more efficient, scalable, and intelligent communication and networking infrastructures. We welcome original research articles and reviews on topics including but not limited to the following: 1. Hardware/Software Co-Design for Diverse Communication Scenarios; 2. Co-Design Solutions for Advanced Networking Paradigms; 3. Hardware Offloading for Network Modules; 4. AI-Driven Co-Design for Communication and Networking; 5. Emerging Architectures and Acceleration Techniques.

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

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