

Special Issue

Parallel and Distributed Cloud, Edge and Fog Computing: Latest Advances and Prospects

Message from the Guest Editors

This Special Issue is soliciting conceptual, theoretical, and experimental contributions to a set of currently unresolved challenges in the area of parallel and distributed cloud, edge and fog computing. The topics of interest include, but are not limited to:

- Distributed resource allocation and scheduling in cloud, edge and fog computing;
- Optimization algorithms for distributed and parallel computing at network infrastructures;
- Network routing for distributed and parallel computing;
- Management and orchestration of distributed computational resources;
- Middleware and libraries for parallel and distributed computing at the cloud, edge and fog layer;
- Development of architectures for parallel and distributed computing;
- Scalability issues in parallel and distributed cloud computing;
- Applications of parallel and distributed computing in next-generation networking infrastructures;
- Security issues during network-enabled parallel and distributed computing;
- Data-resilient, fault-tolerant techniques for intra-infrastructure communication in distributed computing;
- Advanced algorithms for parallelization and distribution of network applications (AI, control theory, etc.).

Guest Editors

Dr. Marios Avgeris

Dr. Dimitrios Dechouniotis

Dr. Konstantinos Tsitseklis

Dr. Vitoropoulou Margarita

Deadline for manuscript submissions

closed (31 December 2024)



Electronics

an Open Access Journal
by MDPI

Impact Factor 2.6
CiteScore 6.1



mdpi.com/si/140218

Electronics
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
electronics@mdpi.com

[mdpi.com/journal/
electronics](https://mdpi.com/journal/electronics)





Electronics

an Open Access Journal
by MDPI

Impact Factor 2.6
CiteScore 6.1



[mdpi.com/journal/
electronics](https://mdpi.com/journal/electronics)



About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and Telecommunications, Politecnico di
Torino, 10129 Torino, Italy

Author Benefits

High Visibility:

indexed within Scopus, SCIE (Web of Science), CAPlus /
SciFinder, Inspec, Ei Compendex and other databases.

Journal Rank:

JCR - Q2 (Engineering, Electrical and Electronic) /
CiteScore - Q1 (Electrical and Electronic Engineering)

Rapid Publication:

manuscripts are peer-reviewed and a first decision is
provided to authors approximately 16.8 days after
submission; acceptance to publication is undertaken in 2.4
days (median values for papers published in this journal in
the first half of 2025).