

Special Issue

Emerging Technologies for Computer Architecture and Parallel Systems

Message from the Guest Editors

This Special Issue aims to gather contributions on the emerging issues, technologies, approaches, techniques, and applications of homogenous or heterogeneous computer architecture (CA) and parallel systems. The topics of interest include, but are not limited to: Design methodologies for computer architectures CAD support for new computer architectures Modeling of computer architectures Application specific computer architectures Heterogeneous computing Approximate computing High performance computing Adaptive and reconfigurable computing High level synthesis tools and applications Evaluation for computer architectures Improved memory architectures IoT, mobile and embedded computer architecture Cloud computer architecture Parallelism in instruction, threads, execution units, coprocessors and data Multicore or multicomputer architectures Optimized input/output computer architectures Quantum computing architectures Computer architectures and memristors CPU interconnectivity and network on chips Computer architecture security and resiliency Fast prototyping computer architectures Verification, evaluation and test of computer architectures

Guest Editors

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

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