# **Special Issue**

# Future Horizons in Analog and Mixed-Signal Systems: Devices, Circuits and Architectures

## Message from the Guest Editors

The scope of this Special Issue includes, but is not limited to, the following topics:

- Analog and mixed-signal circuits for neuromorphic and brain-inspired computing;
- Neuromorphic and AMS processing for robotics and intelligent edge systems;
- Transmitter (TX)/receiver (RX) or front-end architectures for implantable and wearable biomedical devices;
- RF circuits and wireless communication systems;
- Design methodologies addressing scalability, variability, and reliability;
- Efficient power management architectures, including integrated AC-DC/DC-DC converters, low-dropout regulators (LDOs), bandgap references (BGRs), and current/voltage sensors;
- Emerging memory technologies (e.g., RRAM, FeFET) for AMS processing;
- In-memory and near-memory analog computing architectures:
- Device-circuit-architecture co-design methodologies for AMS applications;
- Cross-disciplinary approaches to AMS design, leveraging advances in materials and systems integration.

### **Guest Editors**

Dr. Tejasvi Das

Dr. Scott Koziol

Dr. Sayan Sarkar

## Deadline for manuscript submissions

31 January 2026



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## Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

### Editor-in-Chief

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